Avryl Reill

Circuit Design Engineer

Profile

Dedicated Circuit Design Engineer, with 1 year of experience in designing and validating electronic circuits for diverse industries. Proficient in schematic capture, PCB layout, and simulation tools, with a strong foundation in analog and digital electronics. Adept at collaborating with cross-functional teams to achieve project goals and consistently deliver high-quality designs on schedule. Quick learner with a passion for continuous improvement and innovation in the engineering field.

Employment History

Circuit Design Engineer at ON Semiconductor, ME

Mar 2023 - Present

- Designed a high-speed, low-power integrated circuit (IC) that increased overall performance by 15% while reducing power consumption by 10%, resulting in significant cost savings and improved efficiency for the company's product line.
- Developed an innovative analog-to-digital converter (ADC) with 12-bit resolution and a sampling rate of 5 Mega Samples per Second (MSPS), which outperformed previous designs by 20% and enabled ON Semiconductor to gain a competitive edge in the market.
- Collaborated on a cross-functional team to successfully launch a new microcontroller product that achieved a 25% reduction in development time and 30% improvement in time-to-market, leading to increased revenue and customer satisfaction.

Associate Circuit Design Engineer at , ME

Aug 2022 - Jan 2023

- Developed a 25% more efficient circuit design for a high-speed communication device, resulting in reduced power consumption and increased battery life for the end-users.
- Led a team of 4 engineers to successfully deliver a complex mixed-signal integrated circuit design project within a tight deadline of 6 months, earning recognition from the management for exceptional teamwork and performance.
- Implemented innovative design techniques in analog circuits, leading to a 15% reduction in overall component costs while maintaining high-performance standards.
- Streamlined the design process by creating a reusable library of components and design templates, reducing design time by 30% and improving collaboration among team members.

Education

Bachelor of Science in Electrical Engineering at University of Maine, Orono, ME

Sep 2017 - May 2022

Relevant Coursework: Circuit Analysis, Signals and Systems, Electromagnetic Fields, Digital Logic Design, Microprocessors, Control

Details

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Links

linkedin.com/in/avrylreill

Skills

- SPICE simulation
- VHDL programming
- Verilog coding
- Cadence Virtuoso
- Mentor Graphics
- PCB layout
- SystemVerilog

Languages

- English
- Mandarin

Hobbies

Electronics tinkering and DIY projects

Reading technical journals and books

Attending tech meetups and conferences