Alvaretta Brantingham

DSP Engineer



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4 (301) 630-3998



123 Maple Street, Milwaukee, WI 53202

EDUCATION

Master of Science in Electrical Engineering at University of Wisconsin-Madison, WI

Aug 2017 - May 2022

Relevant Coursework: Advanced Circuit Design, Electromagnetic Theory, Digital Signal Processing, Power Electronics, Communication Systems, Control Systems, VLSI Design, Embedded Systems, Semiconductor Devices, and Renewable Energy Systems.

LINKS

linkedin.com/in/alvarettabrantingham

SKILLS

MATLAB

Simulink

Python

VHDL

Verilog

FPGA programming

C/C++

LANGUAGES

English

Indonesian

HOBBIES

PROFILE

Results-driven DSP Engineer with 1 year of experience in developing and implementing innovative digital signal processing solutions. Proficient in MATLAB, Simulink, and Python, with a solid foundation in signal analysis, algorithm design, and system optimization. Demonstrated ability to collaborate effectively in team settings and contribute to complex projects. Committed to continuous learning and staying current with emerging technologies in the field of digital signal processing.

EMPLOYMENT HISTORY

Senior DSP Engineer at Rockwell Automation, WI

Feb 2023 - Present

- Led the development of an advanced digital signal processing (DSP) algorithm that improved system performance by 35%, resulting in increased customer satisfaction and a 20% boost in sales.
- Successfully managed a team of 10 engineers in the design and implementation of a cutting-edge DSP platform, which reduced product development time by 25% and increased overall efficiency.
- Designed and implemented a custom DSP solution for a high-profile client, resulting in a 30% reduction in energy consumption and saving the client over \$1 million in annual operating costs.
- Played a key role in securing a \$5 million contract by presenting innovative DSP solutions to prospective clients, demonstrating Rockwell Automation's commitment to providing industry-leading technology and services.

DSP Engineer at Plexus Corp, WI

Jul 2022 - Dec 2022

- Successfully designed and implemented a high-performance DSP system for a major client, resulting in a 30% increase in processing speed and a 25% reduction in power consumption.
- Developed and optimized signal processing algorithms for an advanced radar system, leading to a 15% improvement in detection range and accuracy.
- Led a cross-functional team to complete a critical firmware update for a complex medical device, ensuring compliance with industry standards and reducing time-to-market by 20%.

CERTIFICATES

Certified Digital Signal Processing (DSP) Engineer

Oct 2021

Xilinx Certified FPGA Design Professional

Mar 2020

MEMBERSHIPS

Institute of Electrical and Electronics Engineers (IEEE) Signal **Processing Society**