


# Dimple Grehl

## Semiconductor Process Engineer

Detail-oriented Semiconductor Process Engineer with 1 year of experience in optimizing semiconductor manufacturing processes and enhancing production efficiency. Adept at identifying areas for improvement, troubleshooting equipment, and collaborating with cross-functional teams to drive process innovation. Demonstrating a strong foundation in semiconductor principles, materials, and fabrication techniques while committed to maintaining high-quality standards and ensuring continuous process improvement.

[dimple.grehl@gmail.com](mailto:dimple.grehl@gmail.com) 

(558) 616-5540 

123 Maple St, Nashville, TN   
37204

### Education

**Bachelor of Science in  
Electrical Engineering at  
University of Tennessee,  
Knoxville, TN**

Aug 2017 - May 2022

Relevant Coursework: Circuit Analysis, Digital Systems Design, Electronic Devices and Circuits, Control Systems, Signal Processing, Electromagnetic Fields, Power Electronics, Communications Systems, Microprocessors, VLSI Design, and Embedded Systems.

### Links

[linkedin.com/in/dimplegrehl](https://www.linkedin.com/in/dimplegrehl)

### Skills

Photolithography

Etching

Chemical Vapor Deposition  
(CVD)

Physical Vapor Deposition  
(PVD)

Ion Implantation

Rapid Thermal Annealing (RTA)

### Employment History

#### Semiconductor Process Engineer at ON Semiconductor, TN

Feb 2023 - Present

- Achieved a 25% reduction in defect density by optimizing the semiconductor manufacturing process, resulting in improved yield and reduced scrap costs for ON Semiconductor in TN.
- Developed and implemented a new process control plan that increased throughput by 15%, enabling the facility to meet increased customer demand and generate additional revenue.
- Led a cross-functional team in the successful qualification of a new process technology, resulting in a 10% reduction in cycle time and a 5% improvement in overall equipment efficiency.
- Implemented a continuous improvement program that identified and addressed root causes of process variation, resulting in a 20% reduction in process variability and increased process stability.

#### Associate Semiconductor Process Engineer at Skyworks Solutions, TN

Aug 2022 - Jan 2023

- Developed a new semiconductor manufacturing process that increased production efficiency by 25%, leading to a cost saving of \$1.5 million annually for Skyworks Solutions in Tennessee.
- Successfully reduced defect rates in semiconductor products by 30% through the implementation of advanced process control techniques and data-driven optimization, contributing to a 10% increase in overall yield.
- Led a cross-functional team to complete a critical technology transfer project within six months, resulting in the seamless integration of a new product line and generating an additional revenue of \$2 million per year for the company.

### Certificates

#### Certified Semiconductor Process Engineer (CSPE)

Mar 2022

#### Plasma Technology Certification

Sep 2020